A small form factor multi-channel software defined radio frequency (RF) system for commercial and government spectrum applications

**APPLICATIONS**
- Designed for stand-alone missions or integrated as a sub-system component
- REDHAWK FEI 2.0 Compliant Device

**SPECIFICATIONS**
**Size, Weight and Power Specifications**
- Input range: 7V to 15V, nominal 12V
- Backup battery support (CR-123A)
- Typical power consumption: 30W
- Dimensions: 11.2in x 3.44in x 3.33in
- Weight: 4.65 lb
- Operating temperature: -40 to 65°C
- Environment: Designed to meet MIL-STD-810

**Transceiver RF Specifications**
- Tuning range: 70 MHz – 6 GHz
- Tuning step-size: < 3 Hz
- RF channel bandwidth: 200 kHz to 112 MHz
- Typical I/Q balance: > 50 dB
- A/D converter sample rate: 233 Ksamples/sec to 61.44 Msamples/sec
- A/D converter sample width: 12 bits
- RF I/O: SMA (50 ohms)
- Number of RF transceivers: 4 (independently configurable to receive or transmit)
- Receive input: internal limiters allow up to 2W (+33 dBm) survival
- Transmit output: ≥ 0 dBm up to 6 GHz

**Digital Specifications**
- SoC: Altera Arria 10 SX 660 (Dual-Core ARM Cortex A9)
- MCU: Freescale Kinetis K65 MCU (ARM Cortex-M4F) for health and security monitoring
- RAM: 2GB DDR3L-1600 SDRAM with ECC
- Internal flash storage: 64GB eMMC for Linux and root filesystem
- Operating System: Linux Kernel 4.9
  - One 10 gigabit ethernet (10GBASE-SR) through MIL circular connector
  - Dedicated RS-232 UARTs to MCU and SoC
  - 1000BASE-T ethernet for command and control
- IQ Streaming using VITA 49.0 over UDP
- USB
  - USB to UART bridge allows console access to MCU and SoC
  - USB mass storage device interface
  - USB headphone/microphone accessory interface
- Integrated GNSS/GPS receiver with 1PPS for disciplining internal OCXO
  - Optional integrated SAASM device in SRC5986B variant
**Features**

- **Base-Mezzanine Architecture**
  - Internal auxiliary 50 MHz OCXO
  - Common baseband processor card
  - 2x VITA 57.1 FMC expansion sites

- **Baseband Processor Card**
  - 1.5 TFlop Altera Arria 10 SoC
  - Multiple console access via microUSB port
  - High speed RAM and flash access
  - Ultra-low power MCU for health and security monitoring

- **RF Mezzanine**
  - Four RF receive paths (configurable as two separately tunable phase coherent pairs, or four phase coherent channels, using a single LO)
  - 11 selectable sub-octave filters per RF channel
  - 70 – 6000 MHz up to 112 MHz IBW
  - Noise figure 5 dB (50 MHz) to 15 dB (6 GHz)
  - 2W limiter
  - RF loopback for finite calibration of entire signal chain

---

**SRC5986E RUGGED MICRO-TRANSCEIVER**

### RF Subsystem
- RF Front End
  - CH1: AD9361 Transceiver
  - CH2: AD9361 Transceiver

### Signal Processing Subsystem
- System Security
- RAM
- eMMC
- GPS
- Master Reference
- Ethernet

### Optional FMC
- RF Cards (ADC/DACs, Tx/cvrs)
- Co-Processors (GPP, DSP, FPGA)

---

**Contact Information**

800-724-0451 • inquiries@srcinc.com • www.srcinc.com

Scan QR code to download an electronic copy.

© 2019 SRC, Inc. All rights reserved. 20191219